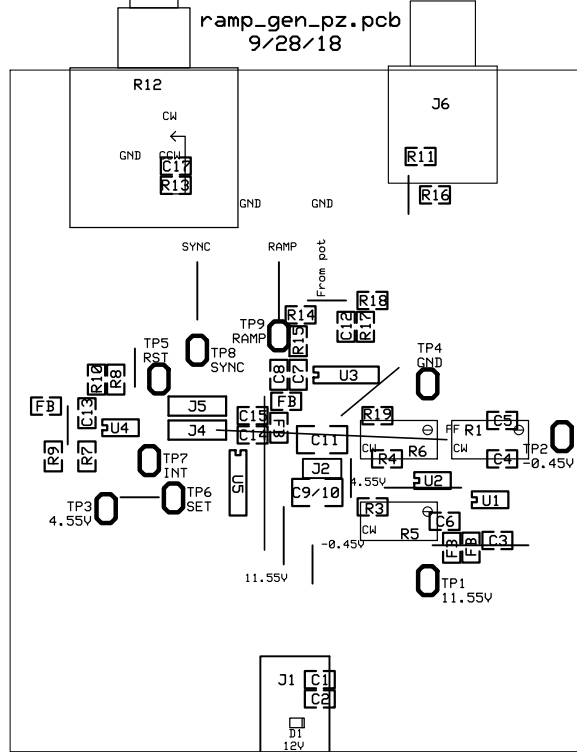
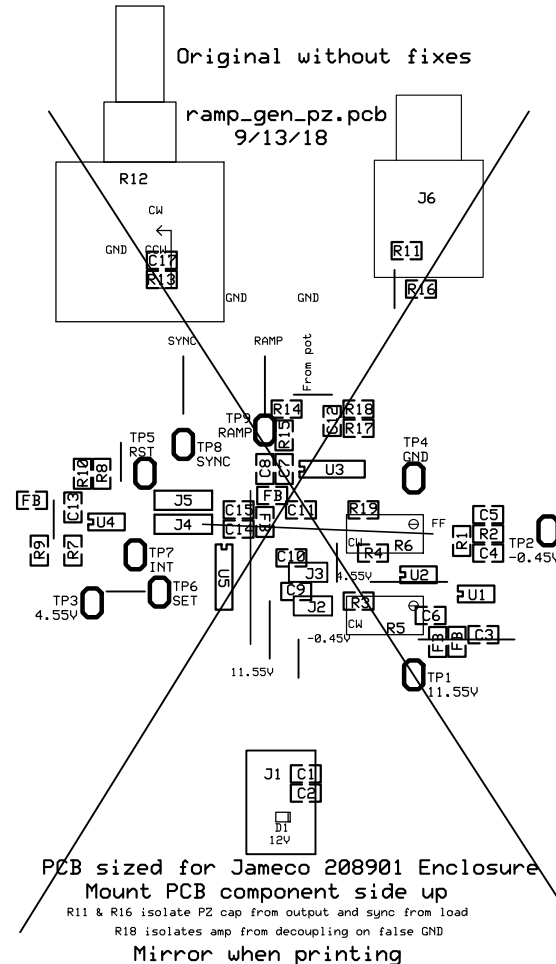


Replaced R1 & R2 with 10K pot to set duty cycle independent of frequency.
 Changed C9-11 to 0.1uF/16V/1210 size ECHU series caps with PPS dielectric (no dc/dv).
 Removed J3 (only two cap timing choices).
 Changed pots to 200K and R3 & R4 to 27K.
 Fixed integrator inputs (U3A).
 Added PCB name & date to copper.
 Made R18 10 times smaller and put it inside the feedback loop (GND had a 200mV ramp).



PCB sized for Jameco 208901 Enclosure
 Mount PCB component side up
 R11 & R16 isolate PZ cap from output and sync from load
 R18 isolates op-amp from decoupling on false GND
 Mirror when printing



PCB sized for Jameco 208901 Enclosure
 Mount PCB component side up
 R11 & R16 isolate PZ cap from output and sync from load
 R18 isolates amp from decoupling on false GND
 Mirror when printing