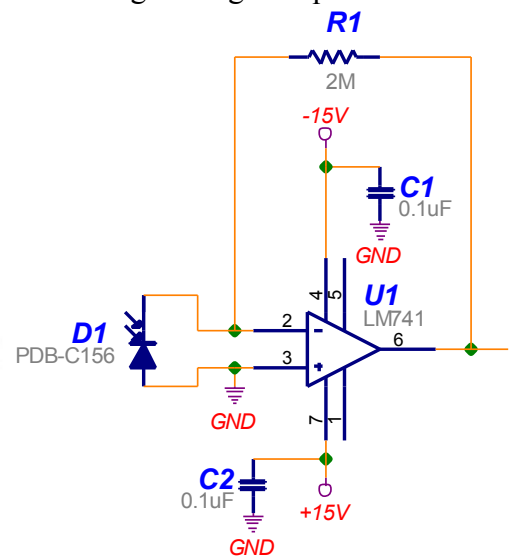
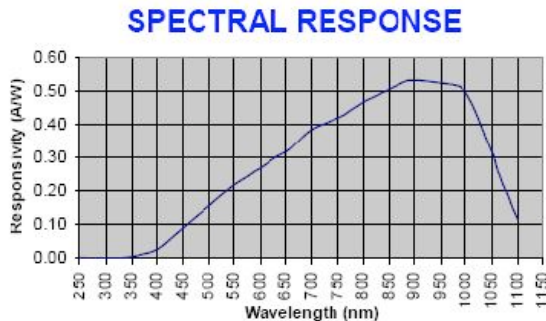


## Homework #8 Photodiode Current to Voltage Converter

1) Design a circuit to amplify the signal from a photodiode. The output should be  $1\text{V}/\mu\text{W}$  at  $850\text{nm}$ . You decide to base your design on a PDB-C156 photodiode and a LM741 op-amp with  $\pm 15\text{V}$  supplies. Note: The light intensity doesn't change quickly so don't worry about filtering out high frequencies or reverse biasing the diode.

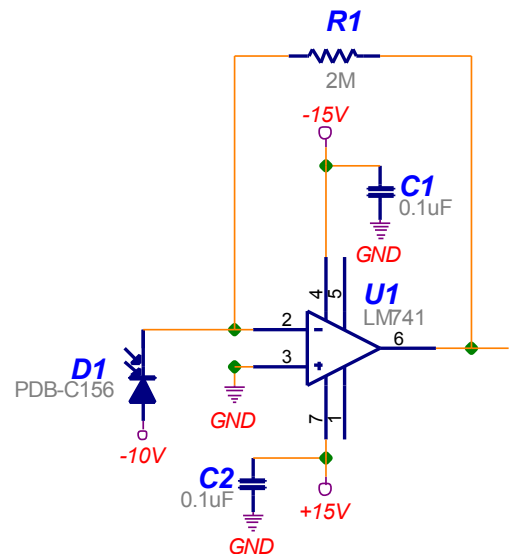
At  $850\text{nm}$  the photodiode puts out  $0.5\text{A}/\text{W}$  ( $0.5\mu\text{A}/\mu\text{W}$ ). We'll use a LM741 configured as a current to voltage converter. To get an output of  $1\text{V}/\mu\text{W}$  from a current of  $0.5\mu\text{A}/\mu\text{W}$  we'll need a  $2\text{M}\Omega$  feedback resistor ( $0.5\mu\text{A}/\mu\text{W} * 2\text{M}\Omega = 1\text{V}/\mu\text{W}$ ). In this circuit the output voltage will go positive when light hits the photodiode. If the photodiode polarity is reversed the output would go negative when light hits the photodiode.



2) You need to do a new experiment and the photodiode must respond faster so you put a  $10\text{V}$  reverse bias on the photodiode.

a) Redraw the circuit with the reverse biased diode.

The circuit is shown to the right. Since the diode can handle a  $50\text{V}$  reverse bias you could safely connect it to  $-15\text{V}$  instead of  $-10\text{V}$ . I used  $-10\text{V}$  because the datasheet lists specs for a  $10\text{V}$  reverse bias. Again, the output will be positive with the given diode polarity.



b) About how large will the **dark current** be and what will the **output voltage** be when no light is hitting the photodiode (use the same feedback resistor as above)? You may assume an ideal op-amp (i.e. the input offset voltage and bias currents are zero).

The datasheet lists the dark current as typically  $2\text{nA}$  ( $15\text{nA}$  max) with a  $10\text{V}$  reverse bias. With the  $2\text{M}\Omega$  feedback resistor the dark current would cause the output to be  $4\text{mV}$  ( $30\text{mV}$  max).

c) Make a guess at the **response time** of your amplifier design with this photodiode and feedback resistor. You may assume an ideal op-amp (i.e. the input capacitance is zero).

The datasheet lists the diode capacitance with a  $10\text{V}$  reverse bias as  $10\text{pF}$  ( $15\text{pF}$  max). With a  $2\text{M}\Omega$  feedback resistor the response time would be about  $20\mu\text{s}$  ( $30\mu\text{s}$  max).

Note: The LM741 is a poor choice for a photodiode amplifier because of the high bias currents and input offset voltages. A FET input op-amp such as OPA128 would be a better choice with  $30\text{fA}$  bias current.

<http://www.ti.com/lit/ds/symlink/opa128.pdf>